

**AMENDMENTS TO THE CLAIMS**

The listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims**

Claims 1-4 (Cancelled)

5. (Currently Amended) A display device according to claim 11, further comprising:

a first memory for keeping the display data therein; and

a second memory for keeping the blanking data therein, wherein:

the control circuit reads the display data from the first memory at timing synchronized with the first clock signal, outputs the display data to the data driver, reads from the second memory the blanking data during one clock signal period in the first half of said two clock signal period at every n signal creation, ~~corresponding to spaces where said second clock signal is not created, and~~ outputs the blanking data to the data driver, and reads from the first memory the display data during the first half of said two clock signal period at every n signal creation and outputs the display data to the data driver.

6. (Previously Presented) A display device according to claim 11, wherein a period of the first clock signal and a period of the second clock signal are synchronized with a scanning period for the scan driver to select pixels of at least one of the rows of pixels.

7. (Currently Amended) A display device according to claim 11, wherein:

the scan driver ~~sequentially~~ selects during one clock signal period in the first half of said two clock signal period at every n signal creation, the other pixel or (n+1) row, one row of pixels in response to the second clock signal and selects the pixels twice for each row at a period of one frame in response to the scanning start signal;

~~the scan driver selects n rows of pixels corresponding to spaces where said second clock signal is not created;~~

~~the data driver supplies the tone voltage corresponding to the display data to the pixels of one row in response to the first clock signal; and~~

~~the data driver supplies the tone voltage corresponding to the blanking data to the pixels of n rows.~~

8. (Currently Amended) A display device according to claim 11, wherein the control circuit outputs to the scan driver, during one clock signal period in the first half of said two clock signal period at every n signal creation, a first scanning enable signal to validate ~~invalidate~~ selection of the pixels by the scan driver ~~corresponding to spaces where said second clock signal is not created~~ and a second scanning enable signal to validate selection of the pixels by the scan driver ~~corresponding to spaces where said second clock signal is not created~~.

Claims 9-10 (Cancelled)

11. (Currently Amended) A display device, comprising:

a pixels array including a plurality of pixels arranged in a form of a matrix;

a data driver for supplying a tone voltage corresponding to display data to the pixels;

a scan driver for selecting pixels of at least one row to which the tone voltage is to be supplied; and

a control circuit for controlling the data driver and the scan driver,

wherein:

the control circuit outputs a first clock signal and the display data to the data driver;

the control circuit outputs to the scan driver a second clock signal, the second clock signal synchronized with the first clock signal and providing a two clock period at every n signal creation, by way of not being created every n ( $n > 2$ ) signal creation thereof and outputs a scanning start signal generated a plurality of times during one frame period; and

the control circuit outputs to the data driver blanking data other than the display data in place of the display data during one clock signal period in the first half of the two clock signal period at every n signal creation; ~~that corresponds to spaces where said second clock signal is not created~~

the data driver, in accordance with the first clock signal, sequentially supplies tone voltage corresponding to the display data received from the control circuit and tone voltage in accordance with the blanking data received in place of the display data from the control circuit, to the pixels;

the scan driver, in accordance with the second clock signal, sequentially shifts pixel rows to be selected, and during one clock signal period in the first half of the two clock signal at every n signal creation, in addition to pixel rows being sequentially shifted, for selecting the other plurality of pixel rows separated from the pixel rows being sequentially shifted by a plurality of rows; and

the scan driver, in accordance with the scanning start signal, further repeats selecting operation of the pixel rows sequentially shifted in accordance with the second clock signal and selecting operation of the other plurality of pixel rows.

Claims 12-20 (Cancelled)

21. (New) A display device according to claim 11, wherein the n is four.

22. (New) A display device according to claim 11, wherein a time distance of signal generation of the scanning start signal is more than one clock signal period of said first clock signal.